



Team 2

2187-71

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey R. Wilcox Et Al.

§ Group Art Unit: 2187

Serial No.: 10/038,960

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Filed: January 2, 2002

Examiner: Glenn Gossage

For: Method And Apparatus For Reducing Power
Consumption In A Memory Bus Interface By
Selectively Disabling Sense Amplifiers

Atty. Dkt. No.: ITL.0668US
(P12986)

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

JUL 08 2004

Technology Center 2100

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated June 17, 2004 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0668US).

Respectfully submitted,

July 2, 2004
Date

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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Monica Jacobs

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	ATTY DOCKET NO. ITL.0668US (P12986)	SERIAL NO. 10/038,960
	APPLICANT(S): Jeffrey R. Wilcox, et al.	
	FILING DATE: January 2, 2002	GROUP ART UNIT:

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
A.						
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C.						
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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
I. TW459242	10/11/2001	Taiwan			Abstract Only	
J.						
K.						
L.						
M.						
N.						
O.						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

P.	Patent Abstracts of Japan, vol. 1996, no. 04, April 30, 1996 & JP 07 320483 A (NEC IC Microcomputer Systems Ltd.), Dec. 8, 1995, Abstract.
Q.	
R.	
S.	
T.	
U.	
V.	

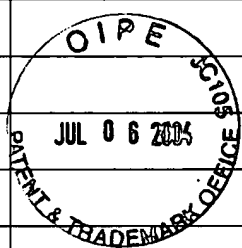
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